

Xilinx - ISE - /home/michel/xilinx/xsa/tp01/tp01.ise - [portes.sch]

File Edit View Project Source Process Add Tools Window Help

Choisir une famille de symboles

Categories: <--All Symbols--> Arithmetic Buffer

Symbols: acc16 acc4

Sym: Choisir un symbole en cliquant sur le nom puis en se déplaçant sur la page de schéma

Orie Rot

Symbol Info

Sou File Snap Libr Syn

Processes for: portes

- Implement Design
- Generate Programming File
- Configure Target Device

Processes Options

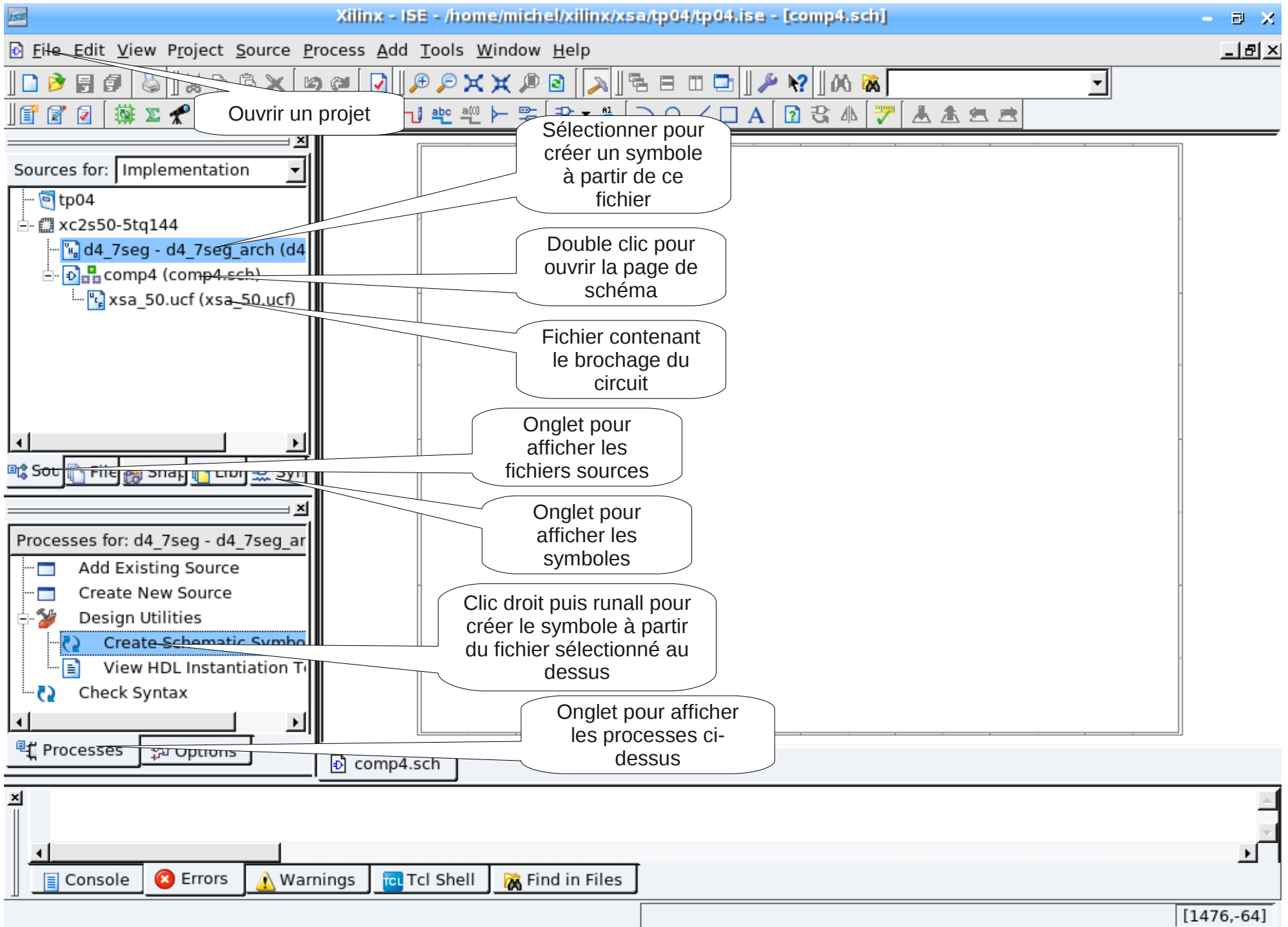
Design Summary portes.sch

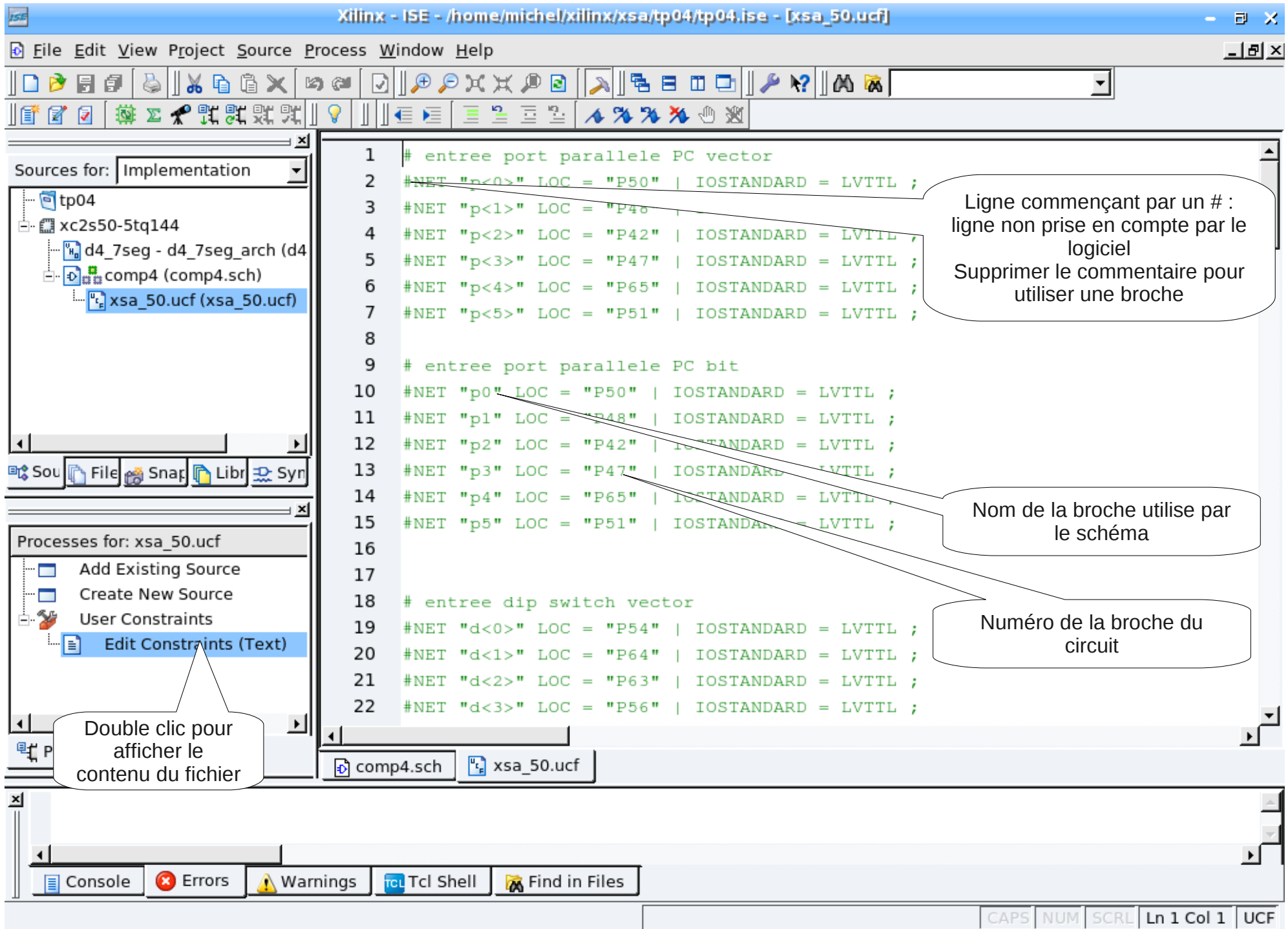
Process "Generate Programming File" completed successfully

Console Errors Warnings Tcl Shell Find in Files

[1432,-52]

The image shows the Xilinx ISE software interface. At the top is a menu bar (File, Edit, View, Project, Source, Process, Add, Tools, Window, Help) and a toolbar with various icons. Below the toolbar is a left-hand panel with a 'Categories' list (All Symbols, Arithmetic, Buffer) and a 'Symbols' list (acc16, acc4). A callout box points to the 'acc16' symbol. Below this is a 'Symbol Info' section. The main workspace displays a logic diagram with several logic gates (AND, OR, NOT) and their interconnections. Callout boxes point to the diagram with the text 'Sélectionner un symbole', 'Etablir les connexions', and 'Ajouter les broches d'entrée ou sortie'. At the bottom of the workspace, a callout box points to the 'Generate Programming File' process in the 'Processes' list, with the text 'Clic bouton droit puis run ou rerun pour créer le fichier de configuration (.bit)'. The bottom of the window features a 'Process Summary' bar showing 'Generate Programming File' as completed, and a console area with tabs for Console, Errors, Warnings, Tcl Shell, and Find in Files.





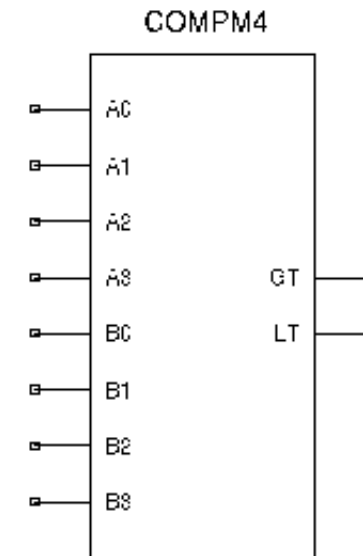
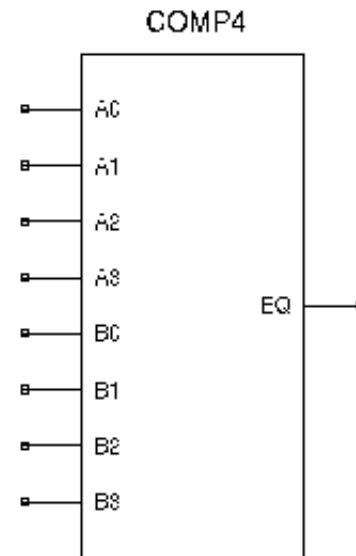
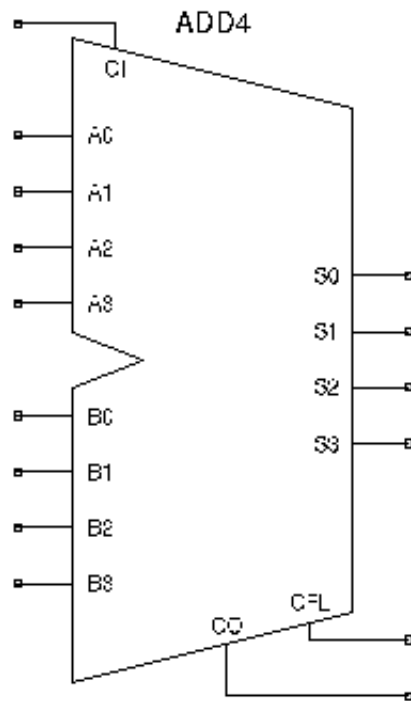
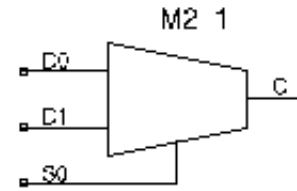
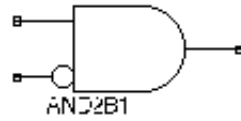
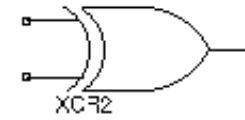
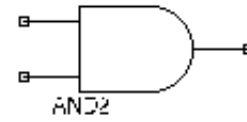
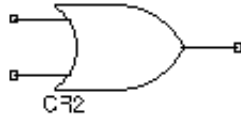
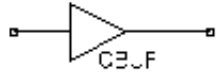
Ligne commençant par un # :
ligne non prise en compte par le
logiciel
Supprimer le commentaire pour
utiliser une broche

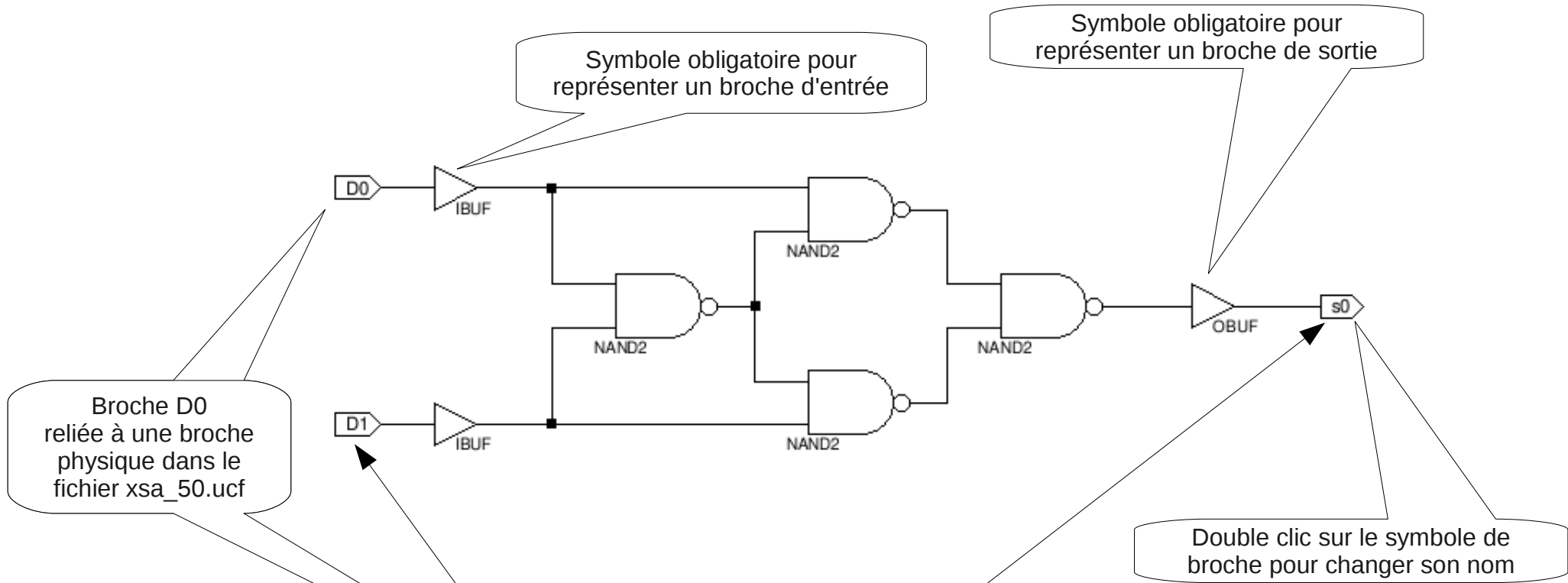
Nom de la broche utilise par
le schéma

Numéro de la broche du
circuit

Double clic pour
afficher le
contenu du fichier

Exemples de symboles





Extrait du fichier : xsa_50.ucf

```

# entree dip switch
NET "D0" LOC = "P54" | IOSTANDARD = LVTTTL ;
NET "D1" LOC = "P64" | IOSTANDARD = LVTTTL ;

# sortie 8 bits data
NET "s0" LOC = "P39" | IOSTANDARD = LVTTTL ;

```